Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-13 (canceled)

Claim 14 (currently amended): An optical receiver circuit, comprising:

a differential amplifier including a first input and a second input;

an optical reception device connected to said first input of said differential amplifier by a first preamplifier, said optical reception device having an electrical behavior in an illumination-free case; ~~and~~

an electrical element for simulating the electrical behavior of said optical reception device in the illumination-free case, said electrical element connected to said second input of said differential amplifier by a second preamplifier; and

said first preamplifier and said second preamplifier being identical transimpedance amplifiers.

Claim 15 (canceled)

Claim 16 (previously presented): The optical receiver circuit according to claim 14, wherein:

said electrical element is formed by a darkened, further reception device.

Claim 17 (previously presented): The optical receiver circuit according to claim 16, wherein:

said optical reception device and said further reception device are monolithically integrated on a chip.

Claim 18 (canceled)

Claim 19 (canceled)

Claim 20 (currently amended): ~~The optical receiver circuit according to claim 18, further comprising:~~An optical receiver circuit, comprising:

a differential amplifier including a first input and a second input;

an optical reception device connected to said first input of said differential amplifier by a first preamplifier, said optical reception device having an electrical behavior in an illumination-free case;

an electrical element for simulating the electrical behavior of said optical reception device in the illumination-free case, said electrical element connected to said second input of said differential amplifier by a second preamplifier; and

said first preamplifier and said second preamplifier being identical;

an integrated control circuit;

said first preamplifier being a transimpedance amplifier having a feedback impedance with a magnitude being settable by a user via said integrated control circuit; and

said second preamplifier being a transimpedance amplifier having a feedback impedance with a magnitude being settable by a user via said integrated control circuit.

Claim 21 (previously presented): The optical receiver circuit according to claim 20, wherein:

said integrated control circuit is connected symmetrically to said feedback impedance of said first preamplifier and to said feedback impedance of said second preamplifier.

Claim 22 (previously presented): The optical receiver circuit according to claim 14, wherein:

said optical reception device and said electrical element are connected to a common supply voltage.

Claim 23 (previously presented): The optical receiver circuit according to claim 22, further comprising:

a low-pass filter connected to the common supply voltage.

Claim 24 (previously presented): The optical receiver circuit according to claim 14, wherein:

said optical reception device is a photodiode; and

said electrical element is a photodiode.

Claim 25 (previously presented): The receiver circuit according to claim 14, further comprising:

a package for packaging said differential amplifier, said optical reception device, and said electrical element, said package being selected from the group consisting of a TO-46 package, a TSSOP10 package, and a VQFN20 package.

Claim 26 (previously presented): The receiver circuit according to claim 25, further comprising:

an integrated control circuit having a control terminal, said package having a terminal pin forming said control terminal.

Claim 27 (new): The optical receiver circuit according to claim 20, wherein:

said electrical element is formed by a darkened, further reception device.

Claim 28 (new): The optical receiver circuit according to claim 27, wherein:

said optical reception device and said further reception device are monolithically integrated on a chip.

Claim 29 (new): The optical receiver circuit according to claim 20, wherein:

said optical reception device and said electrical element are connected to a common supply voltage.

Claim 30 (new): The optical receiver circuit according to claim 29, further comprising:

a low-pass filter connected to the common supply voltage.

Claim 31 (new): The optical receiver circuit according to claim 20, wherein:

said optical reception device is a photodiode; and

said electrical element is a photodiode.

Claim 32 (new): The receiver circuit according to claim 20, further comprising:

a package for packaging said differential amplifier, said optical reception device, and said electrical element, said package being selected from the group consisting of a TO-46 package, a TSSOP10 package, and a VQFN20 package.

Claim 33 (new): The receiver circuit according to claim 32, further comprising:

an integrated control circuit having a control terminal, said package having a terminal pin forming said control terminal.